

Algorithm & SoC Design For Automotive Vision Systems: For Smart Safe Driving System

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Ee382v: system-on-a-chip (soc) design

EE382V: System-on-a-Chip (SoC) Design Andreas Gerstlauer Left-edge algorithm for coloring of interval graphs Data Flow Graphs (DFGs) EE382V: System-on-Chip

Ucsb soc design and test lab

Welcome to UCSB SoC design and test lab! This lab was founded by Professor Kwang-Ting (Tim) Cheng in 1993. We focus on developing methodologies, algorithms

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